Progress on development of Almeo Research XIIP Core System.

The XIIP is currently under development. Prototype hardware has been mostly assembled and tested.
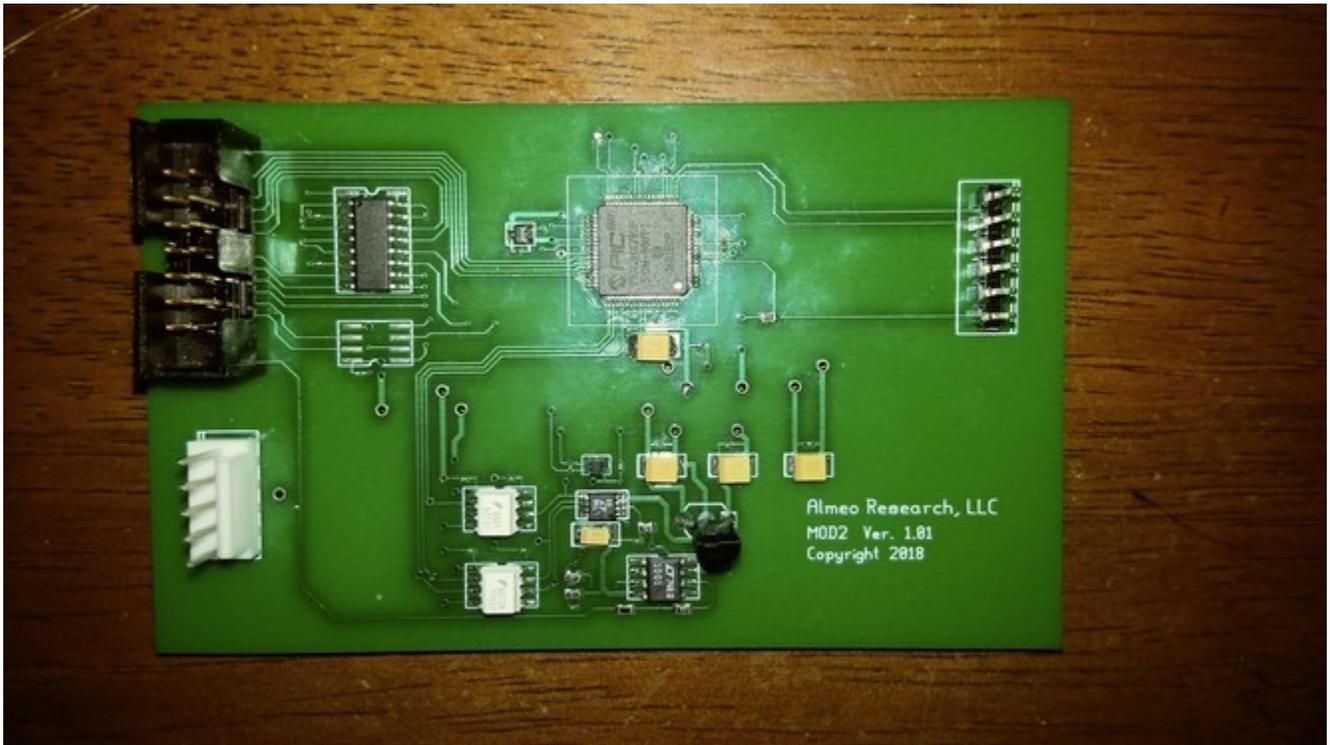It passes all tests.   Figure 1 shows the populated PCB.

Figure 1. Populated PCB.

Figure 2 shows machined, labeled enclosure and the wiring between the PCB and the connector.

Figure 2. Enclosure and wiring.

The enclosure has slots for two circuit boards. A piece of perf board is used for the second circuit board with a Vdrive USB flash drive interface fastened to it. Other components that might be needed can be connected on the perf board. Therefore, the system supports some capability for improvised expandability. See Figure 3.
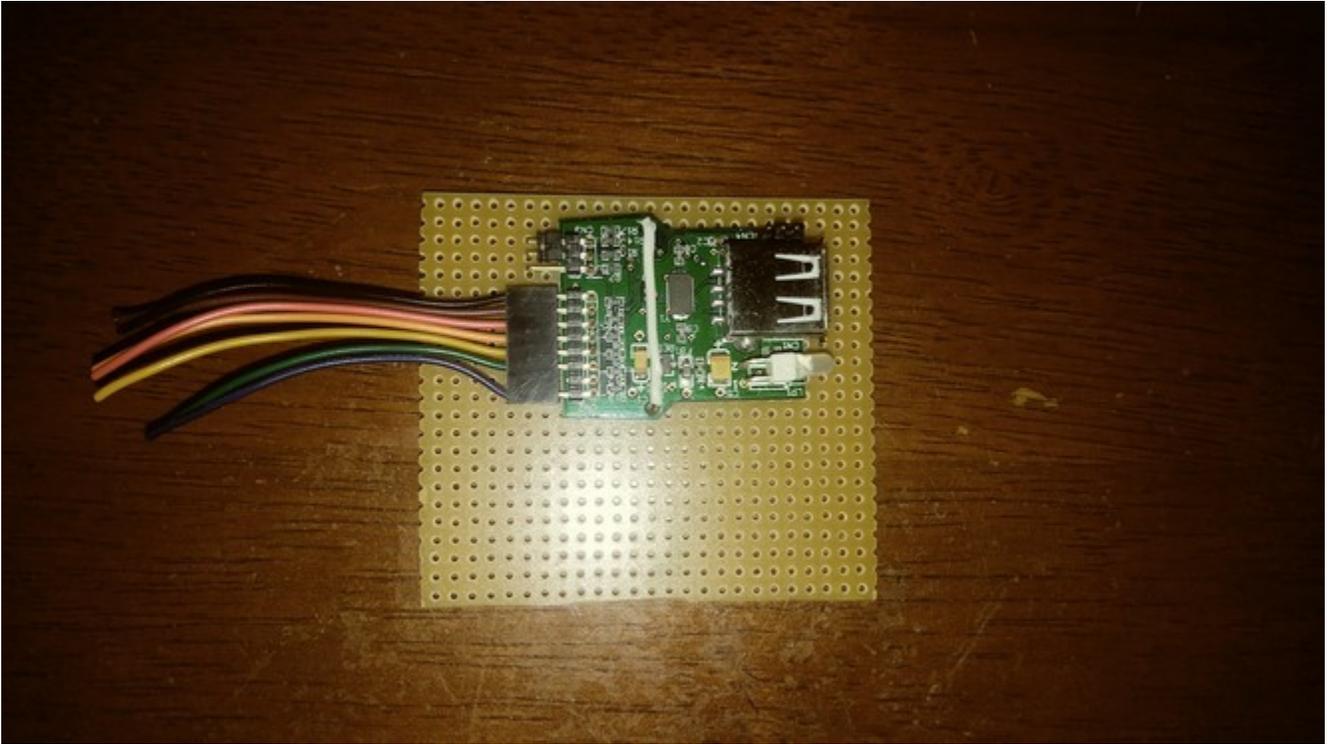

Figure 3.   FTDI   Vdrive and perf board.